Reg. No: SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS) B.Tech III Year II Semester Supplementary Examinations February-2022 **OBJECT ORIENTED ANALYSIS AND DESIGN** (Common to CSE & CSIT) Time: 3 hours Max. Marks: 60 (Answer all Five Units $5 \times 12 = 60$ Marks) UNIT-I a Explain the Modeling system architecture of UML? **6M b** Explain object-oriented modeling **6M** OR Define a model. Explain the (i)importance (ii)principles of modeling. **12M** UNIT-II a Explain Modeling the Distribution of Responsibilities in a System **6M b** Identify the importance of interfaces and their roles. **6M** a What is Modeling in new semantics? **6M b** Explain the Modeling New Building Blocks. **6M** UNIT-III a Explain in details of activity diagram. **8M b** Distinguish sequence and collaboration diagram. **4M** OR a Explain Roles, links, messages, actions, and sequences of interactions. **8M b** Build the Modeling flows of control. **4M UNIT-IV** a What are States, transitions, and activities? **6M b** Explain the Advanced States and Transitions. **6M** OR a List the Common modeling techniques for processes and threads **6M b** Explain the common modeling techniques for state chart diagrams **6M UNIT-V** a Design a Deployment Diagram for College Management System **6M b** Explain the importance of Component diagrams. **6M** Explain the unified library application in detail. 12M

K10

Q.P. Code: 16CS526

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